

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	4436	latch with capacitor	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/26 12:26
L6	1654	latch with capacitor with output	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/26 12:45
L7	165	latch with capacitor with output with source	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/26 12:26
S18	17	rewritable near2 fuse	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/23 11:11
S19	146122	fuse	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/23 11:11
S20	88220	(non-volatile adj memory or nonvolatile adj memory)	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/23 11:12
S21	3523	(floating adj gate adj transistor or floating-gate adj transistor)	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/23 11:13
S22	442756	latch or flip-flop or flip adj flop	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/23 11:14
S23	134	S19 and S20 and S21 and S22	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/23 11:14
S25	1059	programmable adj fuse	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/23 13:25
S26	516	S25 and latch	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/23 11:53
S29	259	programmable adj fuse and S20	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/23 13:26
S48	566	memory adj fuse	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/24 13:12
S62	80	memory adj fuse and (floating adj gate or floating-gate)	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/24 11:48
S74	88343	(non-volatile adj memory or nonvolatile adj memory)	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/24 13:12

S75	183	memory adj fuse and S74	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/24 13:30
S79	112	327/202.ccls. and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/24 13:43
S80	1960	(master-slave or master adj slave) and S74	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/24 13:48
S81	1274	S80 and reset	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/24 13:44
S83	3525	(floating adj gate adj transistor or floating-gate adj transistor)	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/24 13:48